



# Digital Systems Design with FPGAs and CPLDs

*Jawa B Antoun*

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## **Digital Systems Design with FPGAs and CPLDs** Jawa B Antoun

This course introduces digital systems design with hardware description languages (HDL), programmable implementation technologies, electronic design automation design flows, design considerations and constraints, design for test, system on a chip designs, IP cores, reconfigurable computing, digital system design examples and applications.

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