Verilog Hardware Description Language: Analysis and Design of Digital Systems (Delete(Professional Engineering))

Zainalabedin Navabi

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Hardware description languages are used to design high density VLSI, allowing designers to put millions of transistors on a single substrate. Verilog is closing in on VHDL as the most popular of the hardware description languages. This is a tutorial in designing with Verilog. Each major chapter features in-depth Verilog examples of the coding described, and all the examples are assembled into a final full CPU design.

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