



Computer Arithmetics for Nanoelectronics

Vlad P. Shmerko, Svetlana N. Yanushkevich, Sergey Edward Lyshevski

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Emphasizes the Basic Principles of Computational Arithmetic and Computational Structure Design

Taking an interdisciplinary approach to the nanoscale generation of computer devices and systems,

Computer Arithmetics for Nanoelectronics develops a consensus between computational properties provided by data structures and phenomenological properties of nano and molecular technology.

Covers All Stages of the Design Cycle, from Task Formulation to Molecular-Based Implementation

The book introduces the theoretical base and properties of various data structures, along with techniques for their manipulation, optimization, and implementation. It also assigns the computational properties of logic design data structures to 3D structures, furnishes information-theoretical measures and design aspects, and discusses the testability problem. The last chapter presents a nanoscale prospect for natural computing based on assorted computing paradigms from nature.

Balanced Coverage of State-of-the-Art Concepts, Techniques, and Practices

Up-to-date, comprehensive, and pragmatic in its approach, this text provides a unified overview of the relationship between the fundamentals of digital system design, computer architectures, and micro- and nanoelectronics.



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